## APPLICATION DATA SHEET

Applicants:

1<sup>st</sup> Inventor:

Mehrdad Mahanpour

Residence:

4411 Pomponi Street

Union City, Alameda County, California 94587

Mailing:

Same as above

2<sup>nd</sup> Inventor:

Mohammad Massoodi

Residence:

1212 Eureka Court

Los Altos, Santa Clara County, California 94024

Mailing:

Same as above

1<sup>st</sup> Inventor:

Caiwen Yuan

Residence:

34749 Chesapeake Drive

Union City, Alameda County, California 94587

Mailing:

Same as above

Correspondence:

Kelly K. Kordzik

Winstead Sechrest & Minick P.C.

P.O. Box 50784 1201 Main Street

Dallas, Texas 75250-0784

**Application Information:** 

Title: METHOD SYSTEM AND **FOR PROVIDING** 

BACKSIDE VOLTAGE CONTRAST FOR SILICON ON

**INSULATOR DEVICES** 

Classification:

Class:

Subclass:

Tech Center:

Drawing Sheets: 8

Drawing to be Published:

Docket No:

H0921 Utility

Type: Secrecy:

None

Representative Information: Kelly K. Kordzik, Reg. No. 36,571

Barry S. Newberger, Reg. No. 41,527 Robert A. Voigt, Jr., Reg. No. 47,159 Richard F. Frankeny, Reg. No. 47,573

**Domestic Priority:** 

Application:	Continuity Type:	Parent Application:	Parent Filing Date:
None			

**Foreign Priority Information** 

Country:	<b>Application Number:</b>	Filing Date:	Priority Claimed:
None			,
1			

...H0921 PATENT

Assignee:

Advanced Micro Devices, Inc.

One AMD Place P.O. Box 3453

Sunnyvale, CA 94088

AUSTIN\_1\235605\1 184-P081US